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Robin E. Barnum	(Depositor's name)
	(Signature)
September 28, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/716,493	11/20/2000	Gilbert Laurenti	TI-30026	8620

TITLE OF INVENTION: PROCESSOR WITH POINTER TRACKING TO ELIMINATE REDUNDANT MEMORY FETCHES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	12/20/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
O'BRIEN, BARRY J	2183	712-225000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,  
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1 Robert D. Marshall, Jr.2 W. James Brady, III3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

Texas Instruments Incorporated

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Dallas, TX USA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature Robert D. Marshall, Jr.Date September 28, 2004Typed or printed name Robert D. Marshall, Jr.Registration No. 28,527

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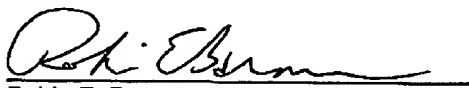
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of **TI-30026**  
Gilbert Laurenti, et al. Art Unit: 2183  
Serial No.: 09/716,493 Examiner: Barry L. O'Brien  
Filed: November 20, 2000 Conf. No.: 8620  
For: Processor With Pointer Tracking to Eliminate Redundant Memory Fetches

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-746-4000 on the date shown below:

  
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<b>NAME OF INVENTOR(S):</b> Gilbert Laurenti, et al.	
<b>TITLE OF INVENTION:</b> Processor With Pointer Tracking to Eliminate Redundant Memory Fetches	
<b>TI FILE NO.:</b> TI-30026	<b>DEPOSIT ACCT. NO.:</b> 20-0668
<b>FAXED:</b> 9/28/04 <b>DUE:</b> 12/20/04 <b>ATTY/SEC'Y:</b> RDM/reb	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.:</b> 09/716,493 <b>Filing Date:</b> November 20, 2000	

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